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Program launch acceleration using **ram cache** - all 2 versions »

CL Ballard, TW Smith - US Patent 5,933,630, 1999 - [freepatentsonline.com](#)

... program are copied from the secondary storage device into **RAM**. ... The **cluster** address is either stored in the log file ... For a **FAT** drive the **cluster** size is stored ...

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... of partition tables, file system directory structures and individual file **cluster** chains in a mass ... - all 2 versions »

TD Harmer - US Patent 6,567,887, 2003 - [freepatentsonline.com](#)

... to describe the use of the read **cache RAM** to **cache** ... is the number of sectors per **cluster**, reserved sectors 1110, number of copies of **FAT** 1112, maximum ...

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The Quadrics Network: High-Performance Clustering Technology - all 63 versions »

F Petri, W Feng, A Hoisie, S Coll, E ... - 2002 - [doi.ieeecomputersociety.org](#)

... are critical components for high-performance **cluster** computers and ... with a 128-entry, 16-bit FIFO **RAM** for flow ... n - 1. Figure 2 shows quaternary **fat** trees of ...

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[CITATION] McKenzie: A Teraflops Linux Beowulf **Cluster** for Computational Astrophysics

J Dubinski, R Humble, C Loken, UL Pen, P Martin - Proc. 17th Ann. Intl. Symp. on High Performance Computing ..., 2003

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Three tier financial transaction system with **cache memory** - all 4 versions »

MP Pearson - US Patent 6,023,634, 2000 - [freepatentsonline.com](#)

... an object oriented database or a **RAM** memory with a ... more than one memory such as a database **cluster**. ... implementing client interface 12 for the **fat** client program ...

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TNet: a reliable system area network - all 9 versions »

RW Hors, TC Inc, CA Cupertino - Micro, IEEE, 1995 - [ieeexplore.ieee.org](#)

... rapid pace of CPU performance enhancements has **fat** exceeded per ... Figure 2. CPU **cluster** connected with the TNet system ... a rolmming table implemented in **RAM**, and a ...

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Divisive clustering of symbolic objects using the concepts of both similarity and dissimilarity - all 2 versions »

K Chidananda Gowda, TV Ravi - Pattern Recognition, 1995 - Elsevier

... Table 3. Description of classes in **fat** oil data **Cluster** sp. io. ... **Cluster** No Samples

Display **RAM** ROM MP Keys ... **Cluster** No Samples Clock General Instrs **Cache Cache** ...

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... , Availability, and Performance in Porcupine: A Highly Scalable, **Cluster**-Based Mail Service - all 70 versions »

Y SAITO, BN BERSHAD, HM LEVY - ACM Transactions on Computer Systems, 2000 - [portal.acm.org](#)

... Performance in Porcupine: A Highly Scalable, **Cluster**-Based Mail Service ... Performance

is linear with respect to the number of nodes in the **cluster**. ...

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[book] Beowulf **Cluster** Computing With Windows - all 4 versions »

T Sterling - 2002 - [books.google.com](#)

... of the Book 10 1 Enabling Technologies 2 An Overview of **Cluster** Computing—Thomas ...

4.2.2 Portability 4.2.3 Symmetric Multiprocessing 4.2.4 **Virtual Memory** 4.2.5 ...

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[book] Tru64 UNIX-Oracle9i **Cluster** Quick Reference - all 2 versions »

T Donar - 2002 - [books.google.com](#)

... AlphaServer Hardware Family Model CPU Speed Max CPUs CPU **Cache** Max **RAM** Max PCI ... clusters

refer to two or more different Alpha systems forming a **cluster**. ...

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